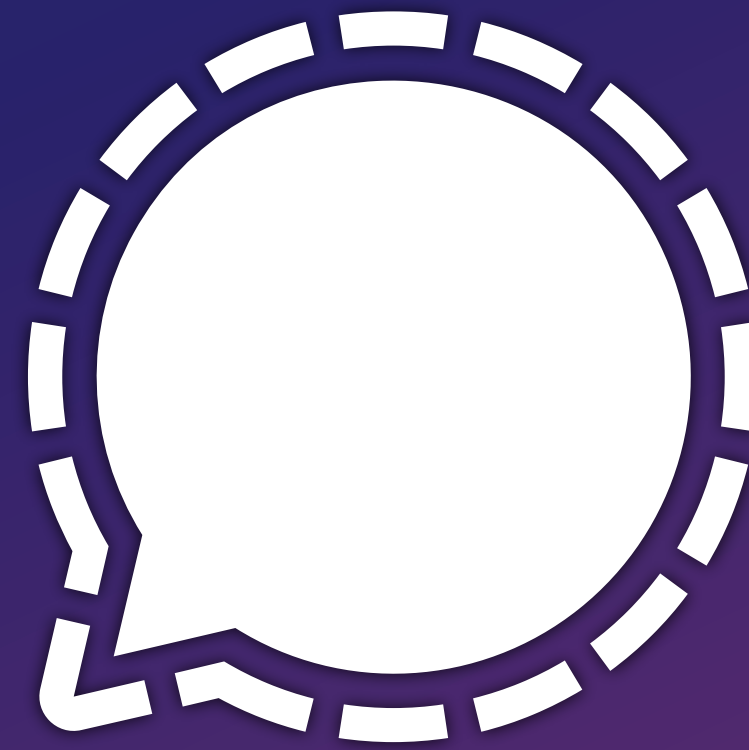


Scaling Security Protocol Verification

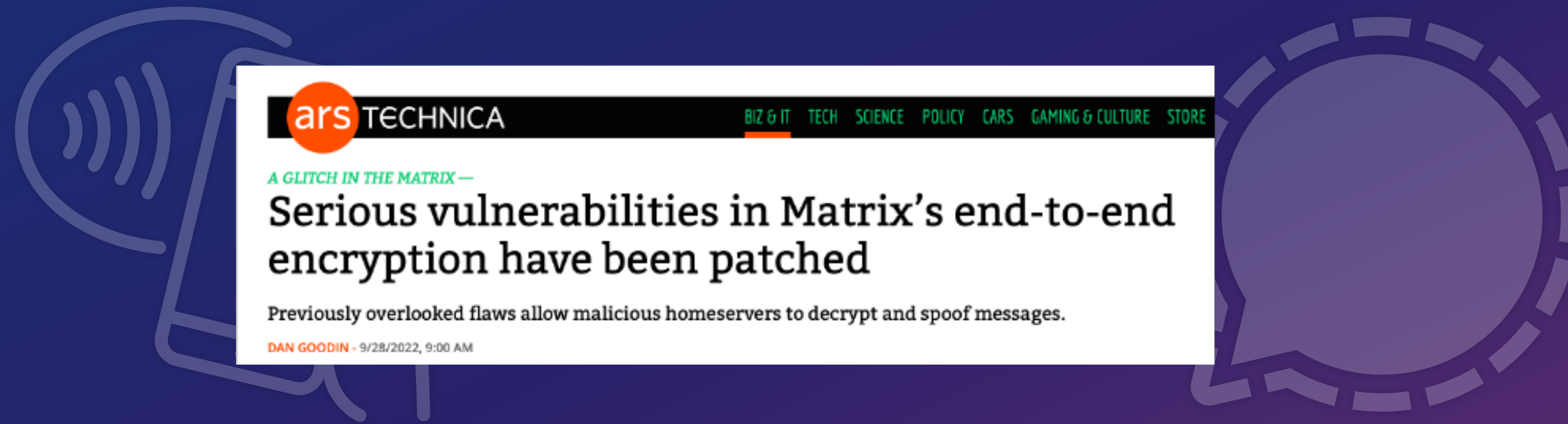
Linard Arquint

Centre for Cyber Trust — 27. Nov. '25

Motivation



Motivation



Motivation



```
if ((err = SSLHashSHA1.update(...)) != 0)
    goto fail;
if ((err = SSLHashSHA1.update(...)) != 0)
    goto fail;
goto fail;
if ((err = SSLHashSHA1.final(...)) != 0)
    goto fail;
```

State of the Art



Protocol model

- Formal protocol description
- Pen and paper proofs
- Automatic tools: Tamarin, ProVerif, ...
- Successfully applied to many protocols

State of the Art



Protocol model

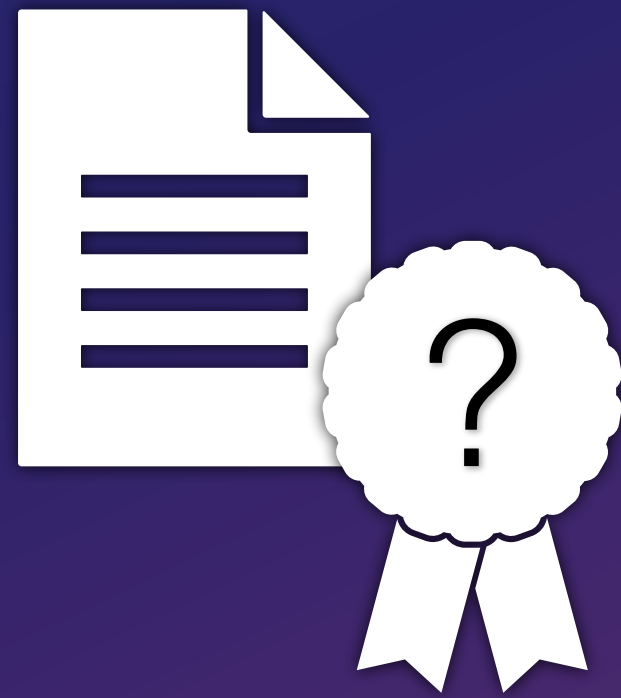
- Formal protocol description
- Pen and paper proofs
- Automatic tools: Tamarin, ProVerif, ...
- Successfully applied to many protocols



Code generation

- Generated code cannot be optimized
- Integration into a codebase is a soundness threat

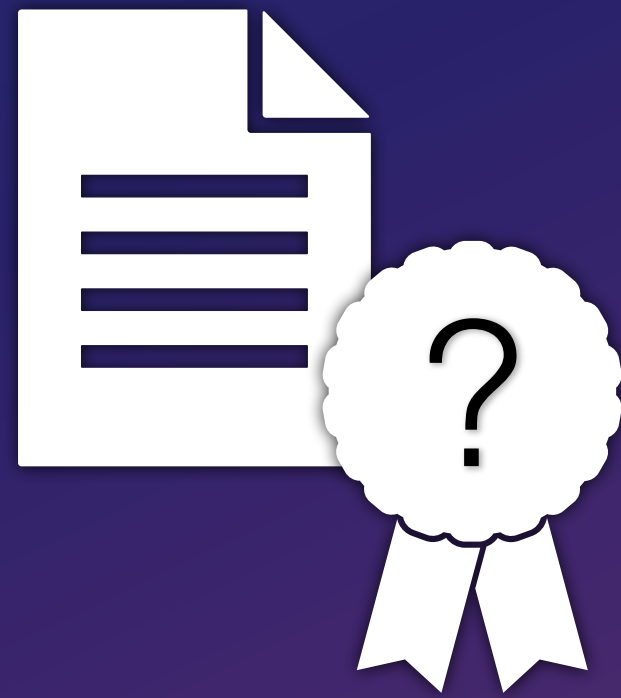
Gap



Existing
implementation

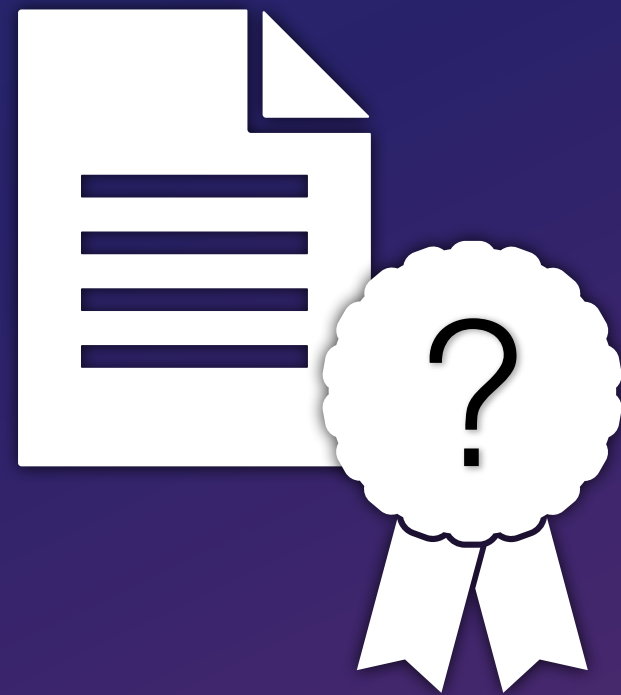
Gap

Correct protocol
implementation?



Existing
implementation

Gap

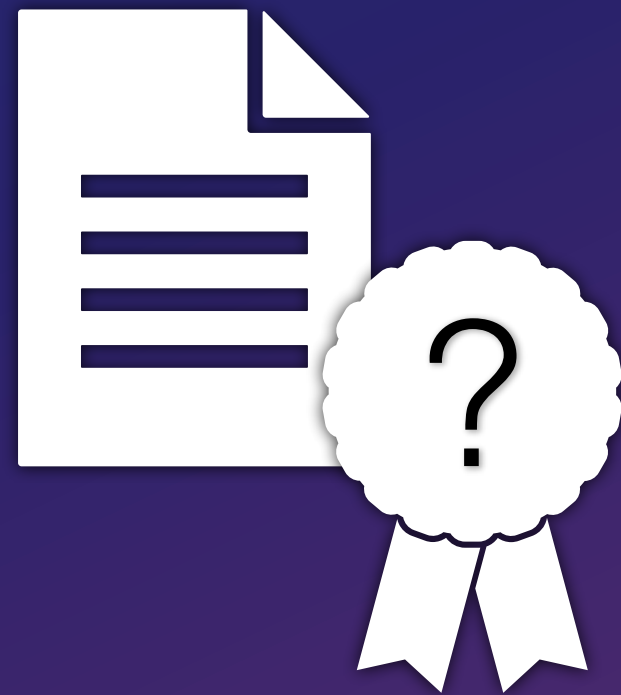


Existing
implementation

Correct protocol
implementation?

Free of buffer overflows?

Gap



Existing
implementation

Correct protocol
implementation?

Free of buffer overflows?

Is it safe, i.e., no panics,
divisions by zero, ...?

Earlier Work



Model refinement

S&P '23

Earlier Work



Model refinement

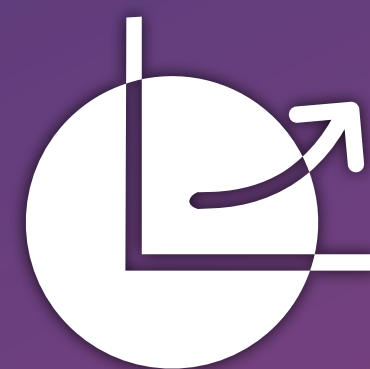
S&P '23



Trace invariant

CCS '23

This Talk



Scaling to large codebases

S&P '26

Problem

Codebase

100k+ LOC

Problem

Codebase

Application

Core

1k LOC

Implements protocol

100k+ LOC

Problem

Codebase
Application

Implements protocol

Core
1k LOC

100k+ LOC

$\subseteq$

Tamarin
model
<I/O spec/>

Problem

Codebase
Application

Implements protocol

Core

1k LOC

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$\subseteq$

$\subseteq ?$

Tamarin
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Problem

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1k LOC

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$\subseteq$

$\subseteq ?$

Tamarin
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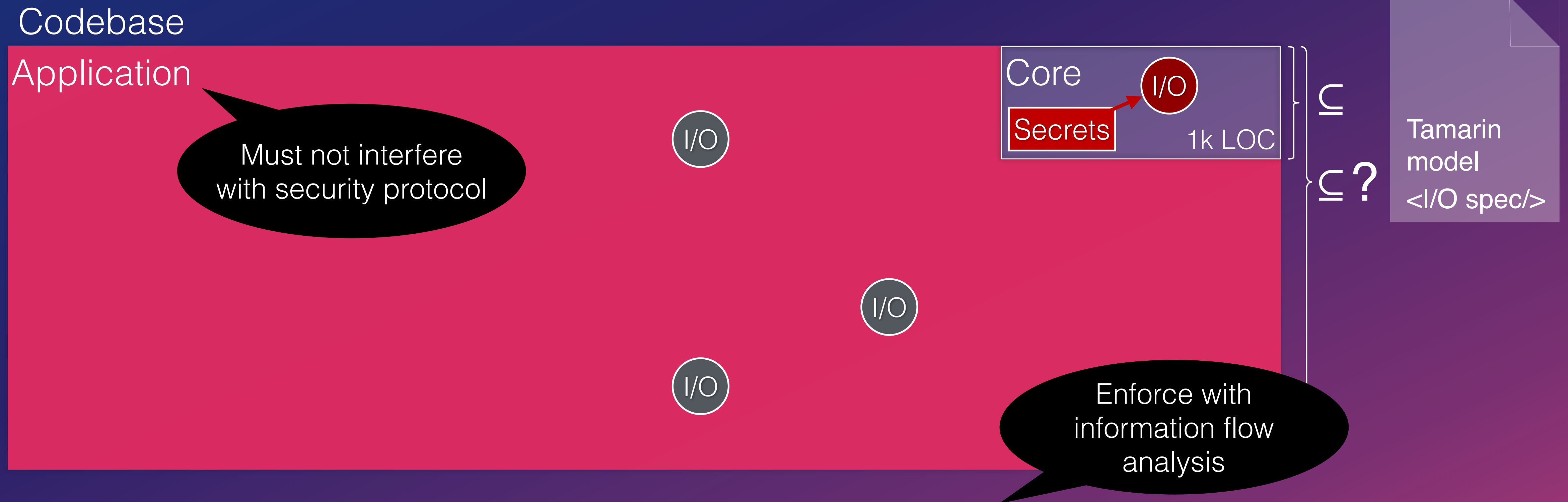
Sources of unsoundness

1. I/O Independence



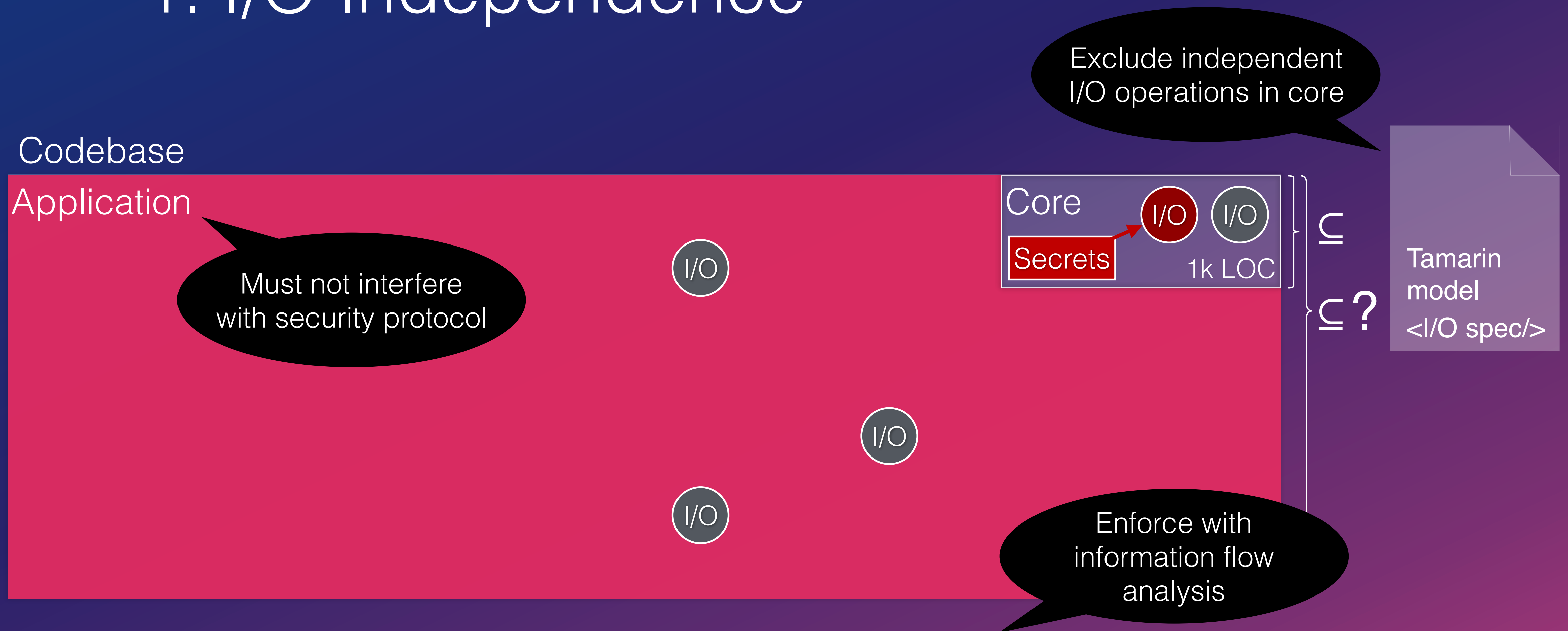
→ I/O operations in application must be independent of core secrets

1. I/O Independence



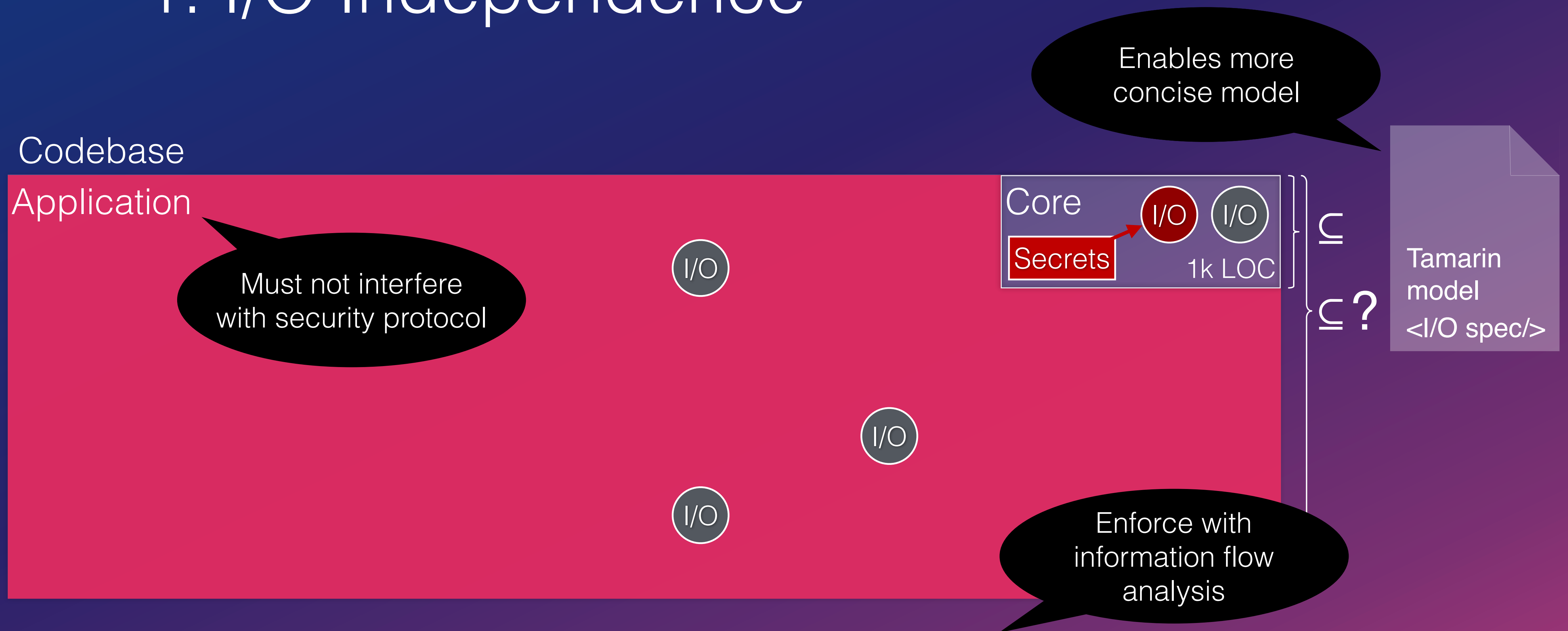
→ I/O operations in application must be independent of core secrets

1. I/O Independence



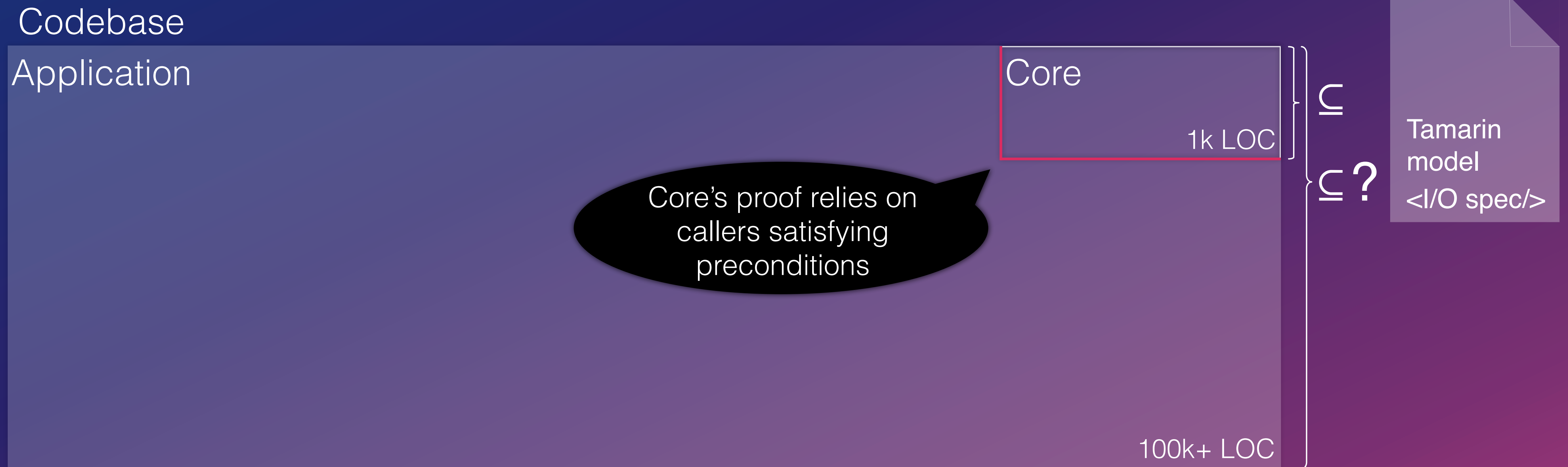
→ I/O operations in application must be independent of core secrets

1. I/O Independence



→ I/O operations in application must be independent of core secrets

2. Discharging Core's Assumptions



→ Syntactically restrict specifications to make them amenable to static analyses

2. Discharging Core's Assumptions

```
//@ requires p != nil ==> acc(p)
//@ ensures  p != nil ==> acc(p)
//@ ensures  c != nil ==> Inv(c)
func NewCore(p *int) (c *Core)
```

```
//@ requires p != nil ==> acc(p)
//@ requires c != nil ==> Inv(c)
//@ ensures  p != nil ==> acc(p)
//@ ensures  c != nil ==> Inv(c)
//@ ensures  r != nil ==> acc(r)
func (c *Core) APIFn(p *int) (r *int)
```

→ Syntactically restrict specifications to make them amenable to static analyses

Soundness

```
func main() {  
  
    c := NewCore()  
  
    a := 42  
  
    c.APIFn(&a)  
  
}
```

```
//@ requires p != nil ==> acc(p)  
//@ ensures p != nil ==> acc(p)  
//@ ensures c != nil ==> Inv(c)  
func NewCore(p *int) (c *Core)
```

```
//@ requires p != nil ==> acc(p)  
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//@ ensures p != nil ==> acc(p)  
//@ ensures c != nil ==> Inv(c)  
//@ ensures r != nil ==> acc(r)  
func (c *Core) APIFn(p *int) (r *int)
```

Soundness

```
func main() {  
  //@ L := {}; I := {}  
  
  c := NewCore()  
  //@ I = I U {c}  
  
  a := 42  
  //@ L = L U {&a}  
  
  //@ L = L \ {&a}  
  //@ I = I \ {c}  
  c.APIFn(&a)  
  //@ L = L U {&a}  
  //@ I = I U {c}  
}
```

```
//@ requires p != nil ==> acc(p)  
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func (c *Core) APIFn(p *int) (r *int)
```

Soundness

```
func main() {  
  //@ L := {}; I := {}  
  
  c := NewCore()  
  //@ I = I U {c}  
  
  a := 42  
  //@ L = L U {&a}  
  
  //@ L = L \ {&a}  
  //@ I = I \ {c}  
  c.APIFn(&a)  
  //@ L = L U {&a}  
  //@ I = I \ {c}  
}
```

```
//@ requires p != nil ==> acc(p)  
//@ ensures p != nil ==> acc(p)  
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//@ requires p != nil ==> acc(p)  
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//@ ensures p != nil ==> acc(p)  
//@ ensures c != nil ==> Inv(c)  
//@ ensures r != nil ==> acc(r)  
func (c *Core) APIFn(p *int) (r *int)
```

Program invariant $p \text{ inv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

Program invariant $\text{pinv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

```
func main() {
  //@ L := {}; I := {}

  c := NewCore()
  //@ I = I U {c}

  a :=  $\frac{x \in L \cup G}{\vdash \{\text{pinv}\} G(*x := e) \{\text{pinv}\}}$ 
  //@ L = L U {&a}

  //@ L = L \ {&a}
  //@ I = I \ {c}
  c.APIFn(&a)
  //@ L = L U {&a}
  //@ I = I \ {c}
}
```

```
//@ requires p != nil ==> acc(p)
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```

Program invariant $\text{pinv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

```
func main() {
  //@ L :=
  c := NewCore()
  //@ I = I U {c}
  a :=  $\frac{x \in L \cup G}{\vdash \{ \text{pinv} \} G(*x := e) \{ \text{pinv} \}}$ 
  //@ L = L \ {&a}
  //@ I = I \ {c}
  c.APIFn(&a)
  //@ L = L U {&a}
  //@ I = I \ {c}
}
```

x is may not alias
core memory

$x \in L \cup G$

$\vdash \{ \text{pinv} \} G(*x := e) \{ \text{pinv} \}$

```
//@ requires p != nil ==> acc(p)
//@ ensures p != nil ==> acc(p)
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func (c *Core) APIFn(p *int) (r *int)
```

Program invariant $\text{pinv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

```
func main() {
  //@ L :=
  c := NewCore()
  //@ I = I ∪ {c}
  a := x ∈ L ∪ G
  //@ ⊢ {pinv} G(*x := e) {pinv}

  //@ L = L \ {&a}
  //@ I = I \ {c}
  c.APIFn(&a)
  //@ L = L ∪ {&a}
  //@ I = I \ {c}
}
```

x is may not alias
core memory

```
//@ requires p != nil ==> acc(p)
//@ ensures p != nil ==> acc(p)
//@ ensures c != nil ==> Inv(c)
func NewCore(p *int) (c *Core)
```

Pointer Analysis

Make sure there are no writes
to memory covered by $\text{Inv}()$

```
//@ requires p != nil ==> acc(p)
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//@ ensures p != nil ==> acc(p)
//@ ensures c != nil ==> Inv(c)
//@ ensures r != nil ==> acc(r)
func (c *Core) APIFn(p *int) (r *int)
```

Program invariant $\text{pinv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

```
func main() {
  //@ L :=
  c := NewCore()
  //@ I = I ∪ {c}

  a :=
  //@ L = L ∪ {&a}
  //@ I = I ∪ {c}
  c.APIFn(&a)
  //@ L = L ∪ {&a}
  //@ I = I ∪ {c}
}
```

x is may not alias
core memory

$x \in L \cup G$

$\vdash \{\text{pinv}\} G(*x := e) \{\text{pinv}\}$

$\frac{\&a \in L \quad c \in I}{\vdash \{\text{pinv}\} G(c.\text{APIFn}(\&a)) \{\text{pinv}\}}$

Pointer Analysis

Make sure there are no writes
to memory covered by $\text{Inv}()$

```
//@ requires p != nil ==> acc(p)
//@ ensures p != nil ==> acc(p)
//@ ensures c != nil ==> Inv(c)
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func (c *Core) APIFn(p *int) (r *int)
```

Program invariant $\text{pinv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

```
func main() {
  //@ L :=
```

x is may not alias
core memory

```
c := NewCore()
//@ I = I ∪ {c}
```

$$\frac{x \in L \cup G}{\vdash \{ \text{pinv} \} G(*x := e) \{ \text{pinv} \}}$$

```
a :=
//@
```

```
//@ L = L \ {&a}
//
```

&a is may not alias core
memory & must not
escape current thread

c is must not escape
current thread

$$\frac{\&a \in L \quad c \in I}{\vdash \{ \text{pinv} \} G(c.\text{APIFn}(\&a)) \{ \text{pinv} \}}$$

Pointer Analysis

Make sure there are no writes
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//@ requires p
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Program invariant $\text{pinv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

```
func main() {
```

```
//@ L :=
```

```
c := NewCore()
```

```
//@ I = I ∪ {c}
```

```
a :=
```

```
//@
```

```
//@ L = L \ {&a}
```

```
//@ I = I \ {c}
```

```
}
```

x is may not alias
core memory

$$\frac{x \in L \cup G}{\vdash \{ \text{pinv} \} G(*x := e) \{ \text{pinv} \}}$$

&a is may not alias core
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escape current thread

c is must not escape
current thread

$$\frac{\&a \in L}{\vdash \{ \text{pinv} \} G(c.\text{APIFn}(\&a)) \{ \text{pinv} \}}$$

$$\frac{c \in I}{\vdash \{ \text{pinv} \} G(c.\text{APIFn}(\&a)) \{ \text{pinv} \}}$$

```
//@ requires p
```

```
//@ ensures p
```

```
//@ ensures c !=
```

```
func NewCore(p *int) (c *Core)
```

```
//@ requires p
```

```
//@ requires p
```

```
//@ ensures p
```

```
//@ ensures c !=
```

```
//@ ensures r !=
```

```
func (c *Core) APIFn(p *int) (r *int)
```

Pointer Analysis

Make sure there are no writes
to memory covered by $\text{Inv}()$

Escape Analysis

Make sure heap locations
remain thread-local

Program invariant $\text{pinv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

```
func main() {
```

```
//@ L :=
```

```
c := NewCore()
```

```
//@ I = I ∪ {c}
```

```
a :=
```

```
//@
```

```
//@ L = L \ {&a}
```

```
//@ I = I \ {c}
```

```
}
```

x is may not alias
core memory

$$\frac{x \in L \cup G}{\vdash \{ \text{pinv} \} G(*x := e) \{ \text{pinv} \}}$$

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&a is may not alias core
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$$\frac{\&a \in L}{\vdash \{ \text{pinv} \} G(c.\text{APIFn}(\&a)) \{ \text{pinv} \}}$$

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```

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//@ requires p
```

```
//@ requires p
```

```
//@ ensures p
```

```
//@ ensures c !=
```

```
//@ ensures r !=
```

```
func (c *Core) APIFn(p *int) (r *int)
```

Pointer Analysis

Make sure there are no writes
to memory covered by $\text{Inv}()$

$$\text{IL} \Rightarrow \text{acc}(p)$$

$$\text{IL} \Rightarrow \text{Inv}(c)$$

$$\text{IL} \Rightarrow \text{acc}(r)$$

$$\text{IL} \Rightarrow \text{Inv}(c)$$

Escape Analysis

Make sure heap locations
remain thread-local

$$\text{IL} \Rightarrow \text{Inv}(c)$$

$$\text{IL} \Rightarrow \text{acc}(r)$$

$$\text{IL} \Rightarrow \text{Inv}(c)$$

Data Race Freedom

Provides linearizability of
heap accesses outside core

Program invariant $\text{pinv} \stackrel{\text{def}}{=} (\forall a \in L. \text{acc}(a)) \star (\forall c \in I. \text{Inv}(c))$

```
func main() {
```

```
//@ L :=
```

```
c := NewCore()
```

```
//@ I = I ∪ {c}
```

```
a :=
```

```
//@
```

```
//@ L = L \ {&a}
```

```
//@ I = I \ {c}
```

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$$\frac{x \in L \cup G}{\vdash \{ \text{pinv} \} G(*x := e) \{ \text{pinv} \}}$$

$\vdash \{ \text{pinv} \} G(*x := e) \{ \text{pinv} \}$

&a is may not alias core
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$$\frac{\&a \in L}{\vdash \{ \text{pinv} \} G(c.\text{APIFn}(\&a)) \{ \text{pinv} \}}$$

$$\frac{c \in I}{\vdash \{ \text{pinv} \} G(c.\text{APIFn}(\&a)) \{ \text{pinv} \}}$$

$\vdash \{ \text{pinv} \} G(c.\text{APIFn}(\&a)) \{ \text{pinv} \}$

```
//@ requires p
```

```
//@ ensures p
```

```
//@ ensures c !=
```

```
func NewCore(p *int) (c *Core)
```

Pointer Analysis

Make sure there are no writes
to memory covered by $\text{Inv}()$

```
//@ requires p
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```
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```
//@ ensures p
```

```
//@ ensures c !=
```

```
//@ ensures r !=
```

```
func (c *Core) APIFn(p *int) (r *int)
```

Escape Analysis

Make sure heap locations
remain thread-local

Data Race Freedom

Provides linearizability of
heap accesses outside core

Soundness

Whole codebase proof

Separation logic proof for the whole codebase with side-conditions dischargeable by static analyses

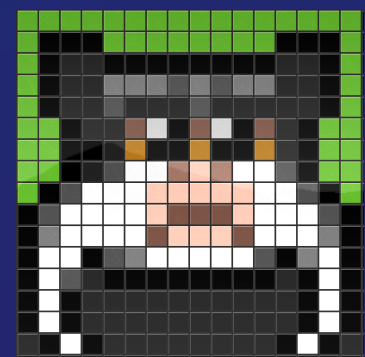
$$\frac{\text{side-conditions}}{\vdash \{ \text{pinv} \} \mathbb{G}(\text{prog}) \{ \text{pinv} \}}$$

ghost code tracking heap locations, core invariant, and I/O specification

I/O independence

Secret-independent I/O operations refine the symbolic attacker

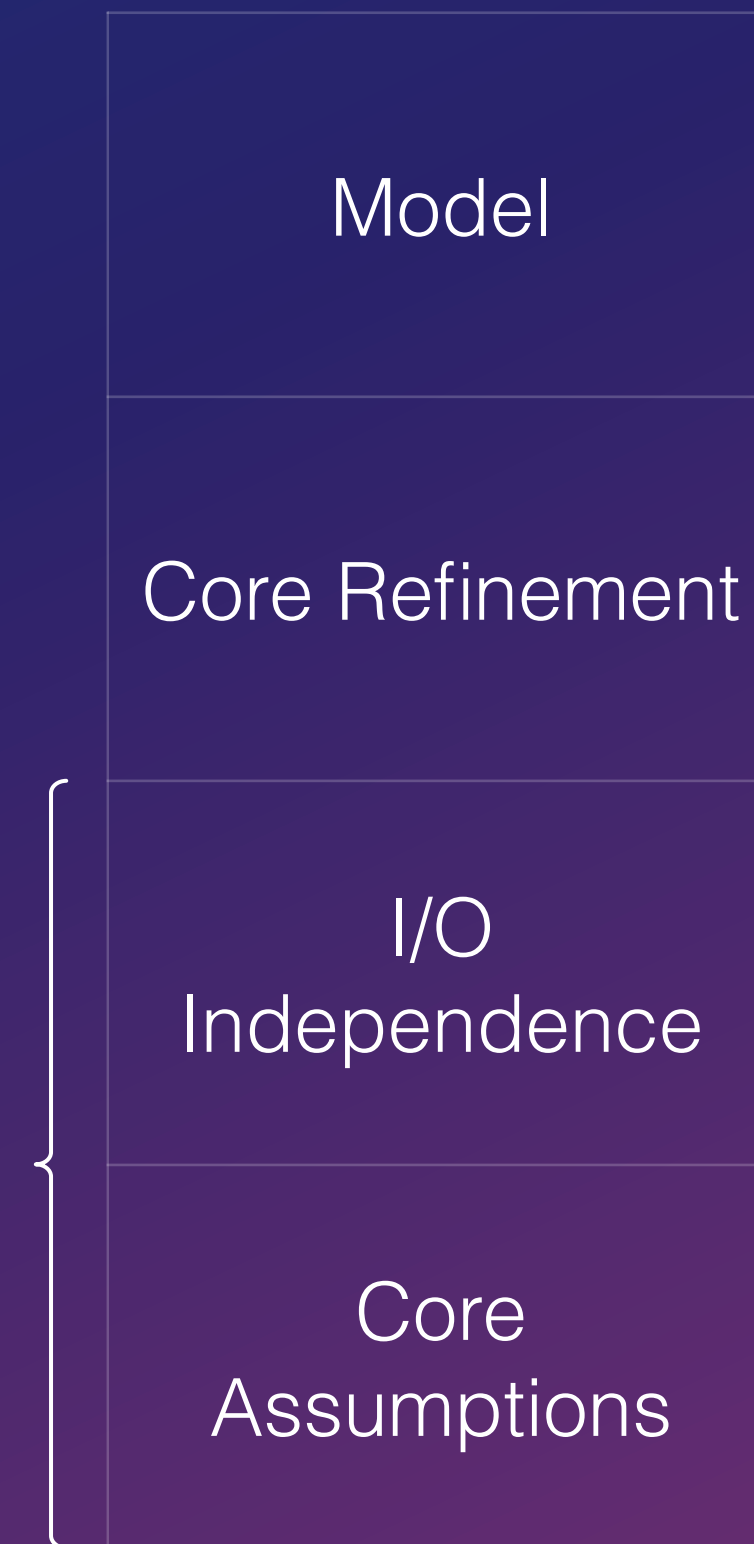
Evaluation — AWS SSM Agent



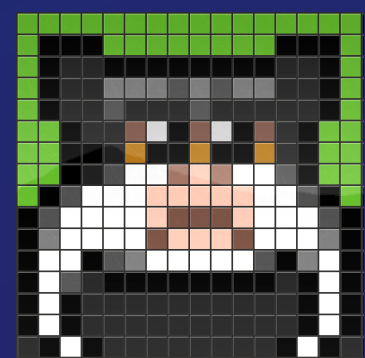
Tamarin

gobra

Argot



Evaluation — AWS SSM Agent



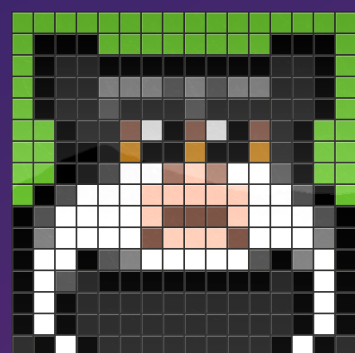
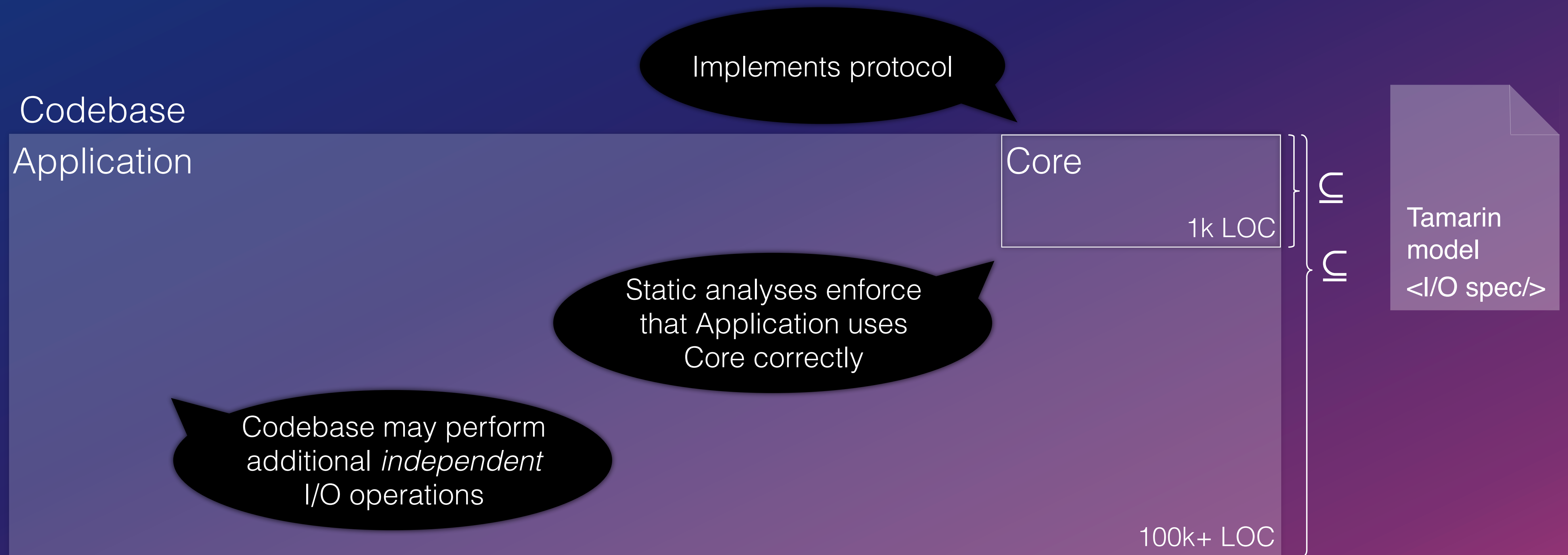
Tamarin

gobra

Argot

	LoC	Proof annotations	Verification time [min]	Effort
Model	319	75	3.3	< 2 pms
Core Refinement	749	2761 + 1064 (generated)	1.2	< 3 pms
I/O Independence	~105k		0.5	< 0.5 pms
Core Assumptions	~105k		2.2	< 1.5 pms

Conclusions



Tamarin

gobra

Argot